SER 1 200 Appli

PATENT 0465-0751P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:

Ha Z. KIM

Conf.:

2187

Appl. No.:

09/820,217

Group:

2822

Filed:

March 29, 2001

Examiner: M. GUERRERO

For:

SEMICONDUCTOR DEVICE AND METHOD FOR

MANUFACTURING THE SAME

LARGE ENTITY TRANSMITTAL FORM

Assistant Commissioner for Patents Washington, DC 20231

September 17, 2002

Sir:

Transmitted herewith is an amendment in the above-identified application.

The enclose	ed document	is being	transmitted	via	the	Certificate
of Mailing	provisions	of 37 C	.F.R. § 1.8.			

The enclosed document is being transmitted via facsimile.

The fee has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	11	-	20	=	0	\$18	\$0.00
INDEPENDENT	2	_	3	=	0	\$84	\$0.00
FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM							\$0.00
						TOTAL	\$0.00

Appl. No. 09/820,217

	Petition for () month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.					
\boxtimes	No fee is required.					
	Check(s) in the amount of \$0.00 is(are) enclosed.					
	Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.					

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

P.O. Box 747

Falls Church, VA 22040-0747

eph A. Kolasch,

(703) 205-8000

ATTACHMENT

JAK/JES:sld

0465-0751P

(Rev. 09/27/01)



#10/Words

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SEMICONDUCTOR DEVICE AND METHOD FOR

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AMENDMENT

Assistant Commissioner for Patents Washington, DC 20231

September 17, 2002

Sir:

In response to the Examiner's Office Action dated June 17,2002, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

In the Claims:

Please cancel claim 9 without prejudice or disclaimer of the subject matter contained therein.

Please amend the claims as follows:

5. (Amended) A method for manufacturing a semiconductor device comprising:

preparing a semiconductor substrate of a first conductivity type;